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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant(s): Stuart D. Downes, Larry Norris, and Jin Liang  
Serial No.: 10/607,400  
For: CIRCUIT BOARD PROCESSING TECHNIQUES USING SOLDER  
FUSING  
Filing Date: June 26, 2003  
Examiner: Johnson, Jonathan J.  
Art Unit: 1725  
Conf. No.: 6196

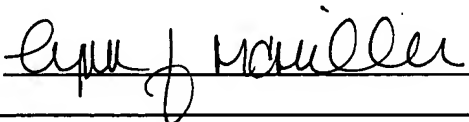
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Date: May 8, 2006

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Signature: 

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**MAIL STOP RCE**  
Commissioner for Patents  
PO Box 1450  
Alexandria, Virginia 22313-1450

**AMENDMENT**

Sir:

In response to the Office Action mailed February 7, 2006, and the Advisory Action mailed on April 17, 2006, please amend the above-identified Application as follows: